


EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	57564	sram	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2006/09/19 14:46
L2	14306	static adj ram	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2006/09/19 14:46
L3	64680	1 or 2	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2006/09/19 14:46
L4	11947	sleep adj mode	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2006/09/19 14:46
L5	1307	3 and 4	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2006/09/19 14:46
L6	79017	"365"/\$.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2006/09/19 14:47
L7	189	5 and 6	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2006/09/19 14:47
L8	244	houston-theodore-w\$.in.	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2006/09/19 14:47

Day : Tuesday
Date: 9/19/2006


PALM INTRANET

Time: 14:51:57

Inventor Name Search Result

Your Search was:

Last Name = HOUSTON

First Name = THEODORE

Application#	Patent#	Status	Date Filed	Title	Inventor Name
10337069	7027346	150	01/06/2003	BIT LINE CONTROL FOR LOW POWER IN STANDBY	HOUSTON, THEODORE W.
10349277	7039818	150	01/22/2003	LOW LEAKAGE SRAM SCHEME	HOUSTON, THEODORE W.
10400353	Not Issued	66	03/27/2003	Hydrogen anneal before gate oxidation	HOUSTON, THEODORE W.
10417581	6974968	150	04/17/2003	METHOD AND APPARATUS FOR FABRICATING SELF-ALIGNED CONTACTS IN AN INTEGRATED CIRCUIT	HOUSTON, THEODORE W.
10417627	Not Issued	71	04/17/2003	Method of Fabrication Etch-Stopped Soi Back-Gate Contact	HOUSTON, THEODORE W.
10616755	6925010	150	07/10/2003	STATIC RANDOM ACCESS MEMORY DEVICE HAVING DECREASED SENSITIVITY TO VARIATIONS IN CHANNEL PHYSICAL CHARACTERISTICS	HOUSTON, THEODORE W.
10618473	Not Issued	71	07/11/2003	System for reducing row periphery power consumption in memory devices	HOUSTON, THEODORE W.
10628148	Not Issued	93	07/28/2003	SEMICONDUCTOR ON INSULATOR DEVICE ARCHITECTURE AND METHOD OF CONSTRUCTION	HOUSTON, THEODORE W.
10635185	Not Issued	41	08/06/2003	Asymmetric static random access memory device having reduced bit line leakage	HOUSTON, THEODORE W.
10664547	Not Issued	61	09/19/2003	Integrated DRAM process/structure using contact pillars	HOUSTON, THEODORE W.
10666336	6800523	150	09/17/2003	INTEGRATED DRAM	HOUSTON,

				PROCESS/STRUCTURE USING CONTACT PILLARS	THEODORE W.
<u>10667615</u>	Not Issued	77	09/22/2003	Multi-gate one-transistor dynamic random access memory	HOUSTON, THEODORE W.
<u>10682729</u>	<u>6873008</u>	150	10/10/2003	ASYMMETRICAL DEVICES FOR SHORT GATE LENGTH PERFORMANCE WITH DISPOSABLE SIDEWALL	HOUSTON, THEODORE W.
<u>10691410</u>	<u>6954918</u>	150	10/22/2003	INTEGRATED CIRCUIT CELLS	HOUSTON, THEODORE W.
<u>10691426</u>	<u>6996787</u>	150	10/22/2003	INTEGRATED CIRCUIT CELLS	HOUSTON, THEODORE W.
<u>10701669</u>	<u>6925025</u>	150	11/05/2003	SRAM DEVICE AND A METHOD OF POWERING-DOWN THE SAME	HOUSTON, THEODORE W.
<u>10704858</u>	<u>6900656</u>	150	11/10/2003	METHOD OF TESTING AN INTEGRATED CIRCUIT AND AN INTEGRATED CIRCUIT TEST APPARATUS	HOUSTON, THEODORE W.
<u>10732970</u>	Not Issued	71	12/11/2003	SRAM device and a method of operating the same to reduce leakage current during a sleep mode	HOUSTON, THEODORE W.
<u>10733038</u>	Not Issued	71	12/11/2003	Static random access memory device having reduced leakage current during active mode and a method of operating thereof	HOUSTON, THEODORE W.
<u>10733485</u>	<u>6922370</u>	150	12/11/2003	HIGH PERFORMANCE SRAM DEVICE AND METHOD OF POWERING-DOWN THE SAME	HOUSTON, THEODORE W.
<u>10745429</u>	<u>6982915</u>	150	12/22/2003	SRAM WITH TEMPERATURE-DEPENDENT VOLTAGE CONTROL IN SLEEP MODE	HOUSTON, THEODORE W.
<u>10818799</u>	Not Issued	161	04/05/2004	Semiconductor device with fully self-aligned local interconnects, and method for fabricating the device	HOUSTON, THEODORE W.
<u>10857420</u>	Not Issued	161	05/27/2004	Semiconductor apparatus having contacts of multiple heights and method of making same	HOUSTON, THEODORE W.
<u>10862637</u>	Not Issued	30	06/07/2004	Statistical evaluation of circuit robustness separating local and global variation	HOUSTON, THEODORE W.
<u>10870355</u>	Not	93	06/17/2004	STAGGERED MEMORY CELL	HOUSTON,

	Issued			ARRAY	THEODORE W.
<u>10895512</u>	Not Issued	30	07/20/2004	Method and system for contiguous proximity correction for semiconductor masks	HOUSTON, THEODORE W.
<u>10945725</u>	Not Issued	95	09/21/2004	SYSTEM FOR REDUCING ROW PERIPHERY POWER CONSUMPTION IN MEMORY DEVICES	HOUSTON, THEODORE W.
<u>10993815</u>	Not Issued	30	11/19/2004	Design method and system for optimum performance in integrated circuits that use power management	HOUSTON, THEODORE W.
<u>11018602</u>	Not Issued	95	12/21/2004	APPLICATION OF POST-PATTERN RESIST TRIM FOR REDUCING POCKET-SHADOWING IN SRAMS	HOUSTON, THEODORE W.
<u>11109224</u>	Not Issued	41	04/18/2005	Bit line control for low power in standby	HOUSTON, THEODORE W.
<u>11143038</u>	Not Issued	41	06/02/2005	Static random-access memory having reduced bit line precharge voltage and method of operating the same	HOUSTON, THEODORE W.
<u>11171033</u>	Not Issued	41	06/30/2005	Area efficient implementation of small blocks in an SRAM array	HOUSTON, THEODORE W.
<u>11191349</u>	Not Issued	41	07/28/2005	Memory array with a delayed wordline boost	HOUSTON, THEODORE W.
<u>11191741</u>	Not Issued	89	07/28/2005	SRAM cell with column select line	HOUSTON, THEODORE W.
<u>11202141</u>	Not Issued	30	08/11/2005	SRAM cell using separate read and write circuitry	HOUSTON, THEODORE W.
<u>11204064</u>	Not Issued	30	08/15/2005	Static random access memory device having a voltage-controlled word line driver for retain till accessed mode and method of operating the same	HOUSTON, THEODORE W.
<u>11234346</u>	Not Issued	30	09/23/2005	Write assist for latch and memory circuits	HOUSTON, THEODORE W.
<u>11234910</u>	Not Issued	20	09/26/2005	Thermostatic biasing controller, method of thermostatic biasing and an integrated circuit employing the same	HOUSTON, THEODORE W.
<u>11237082</u>	Not Issued	30	09/28/2005	Static random access memory device having bit line voltage control for retain till accessed mode and method of operating the	HOUSTON, THEODORE W.

				same	
11238932	Not Issued	30	09/29/2005	SRAM cell with asymmetrical pass gate	HOUSTON, THEODORE W.
11239626	Not Issued	30	09/29/2005	SRAM cell with asymmetrical transistors for reduced leakage	HOUSTON, THEODORE W.
11239834	Not Issued	30	09/30/2005	Sub-lithographics opening for back contact or back gate	HOUSTON, THEODORE W.
11259999	Not Issued	30	10/27/2005	Body bias coordinator, method of coordinating a body bias and sub-circuit power supply employing the same	HOUSTON, THEODORE W.
11268974	Not Issued	20	11/08/2005	Method for testing transistors having an active region that is common with other transistors and a testing circuit for accomplishing the same	HOUSTON, THEODORE W.
60211259	Not Issued	159	06/13/2000	Sram with write-back on read	HOUSTON, THEODORE W.
60235096	Not Issued	159	09/25/2000	Hydrogen anneal before gate oxidation	HOUSTON, THEODORE W.
60244726	Not Issued	159	10/31/2000	Loadless 4T SRAM cell with PMOS drivers	HOUSTON, THEODORE W.
60257326	Not Issued	159	12/20/2000	Semiconductor on insulator device architecture and method of construction	HOUSTON, THEODORE W.
60257726	Not Issued	159	12/21/2000	Method and apparatus for reducing capacitive coupling between lines in an integrated circuit	HOUSTON, THEODORE W.
60258768	Not Issued	159	12/29/2000	Improved static random access memory cell and method	HOUSTON, THEODORE W.

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	<input type="text" value="houston"/>	<input type="text" value="theodore"/>	

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